

Mullard**DOUBLE DIODE OUTPUT PENTODE****PEN40DD**

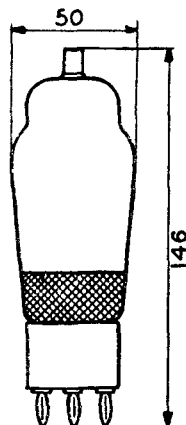
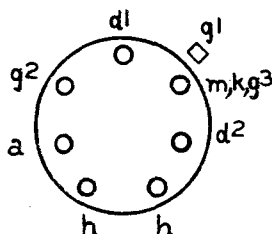
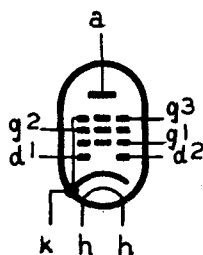
Heater

 $V_f = 44 \text{ V}$ $I_f = 0.2 \text{ A}$

Capacities

 $C_{dd'} = 0.25 \mu\text{F}$ $C_{dk} = 3.1 \mu\text{F}$ $C_{d'k} = 3.2 \mu\text{F}$ $C_{da} = 0.2 \mu\text{F}$ $C_{d'a} = 0.2 \mu\text{F}$ $C_{dgl} = 0.08 \mu\text{F}$ $C_{d'gl} = 0.08 \mu\text{F}$ $C_{agl} = 1.0 \mu\text{F}$ $C_{ak} = 8.4 \mu\text{F}$ $C_{gk} = 9.8 \mu\text{F}$ **Operating Conditions as Class A Amplifier**

V_a	200 V
V_{g2}	200 V
I_a	45 mA
$-V_{g1}$	8.5 V
I_{g2}	6 mA
S_w	8 mA/V
R_i	35,000 Ω
W_o	4 W
R_a	4,500 Ω
$V_{g1\text{eff}}$	5 V
$V_{g2\text{eff}} (50 \text{ mW})$	0.5 V
dt_{tot}	10 %
R_k	170 Ω

Arrangement of electrodes and base connections**PEN 40DD**

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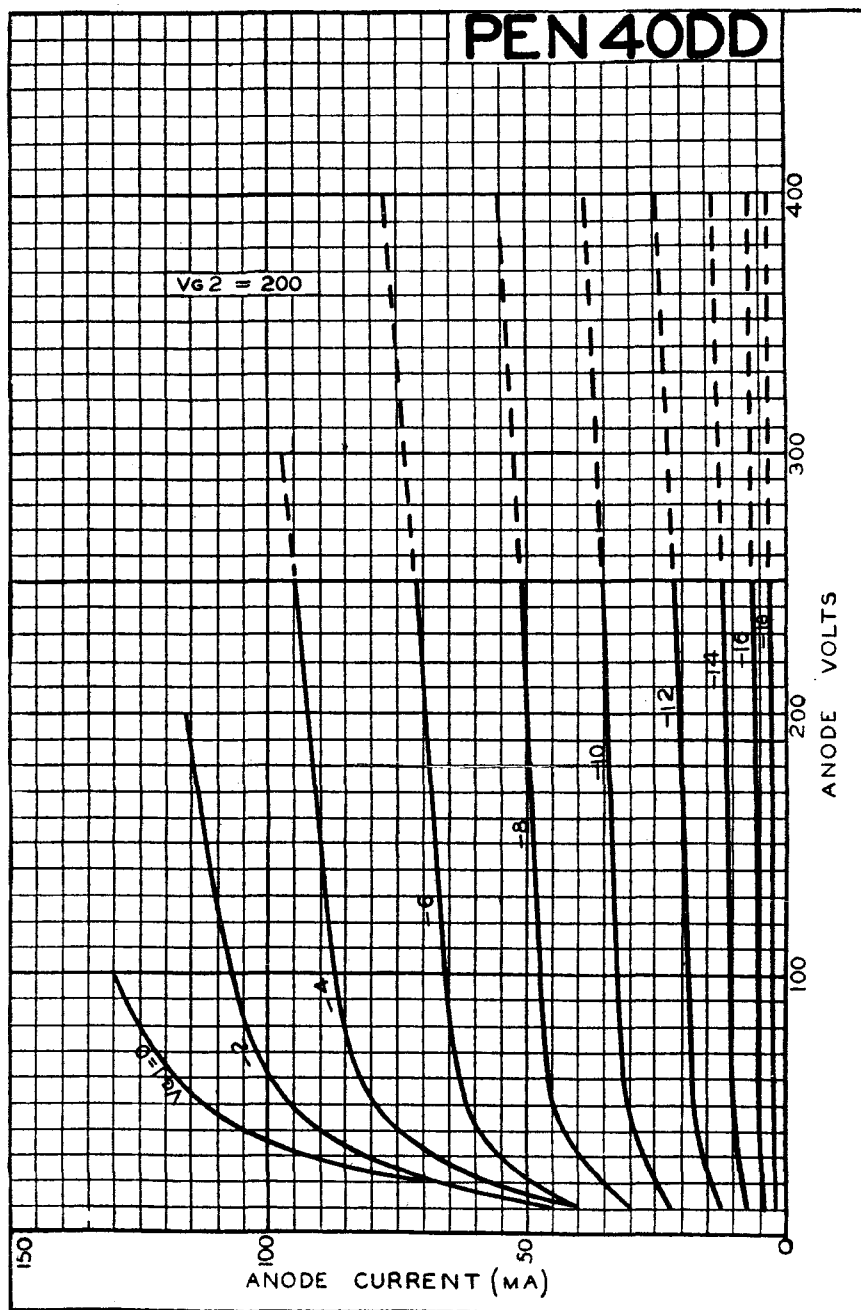
Limiting Values

Va max.	250 V
Wa max.	9 W
Ik max.	70 mA
Vg2 max.	250 V
Wg2 max.	2 W
Rgla max.	1.0 MΩ
Vfk max.	175 V
Rfk max.	5,000 Ω
Vd max.	200 V
Id max.	0.8 mA
Vgl (Igl = 1.0 uA)	-0.3 to -0.8 V

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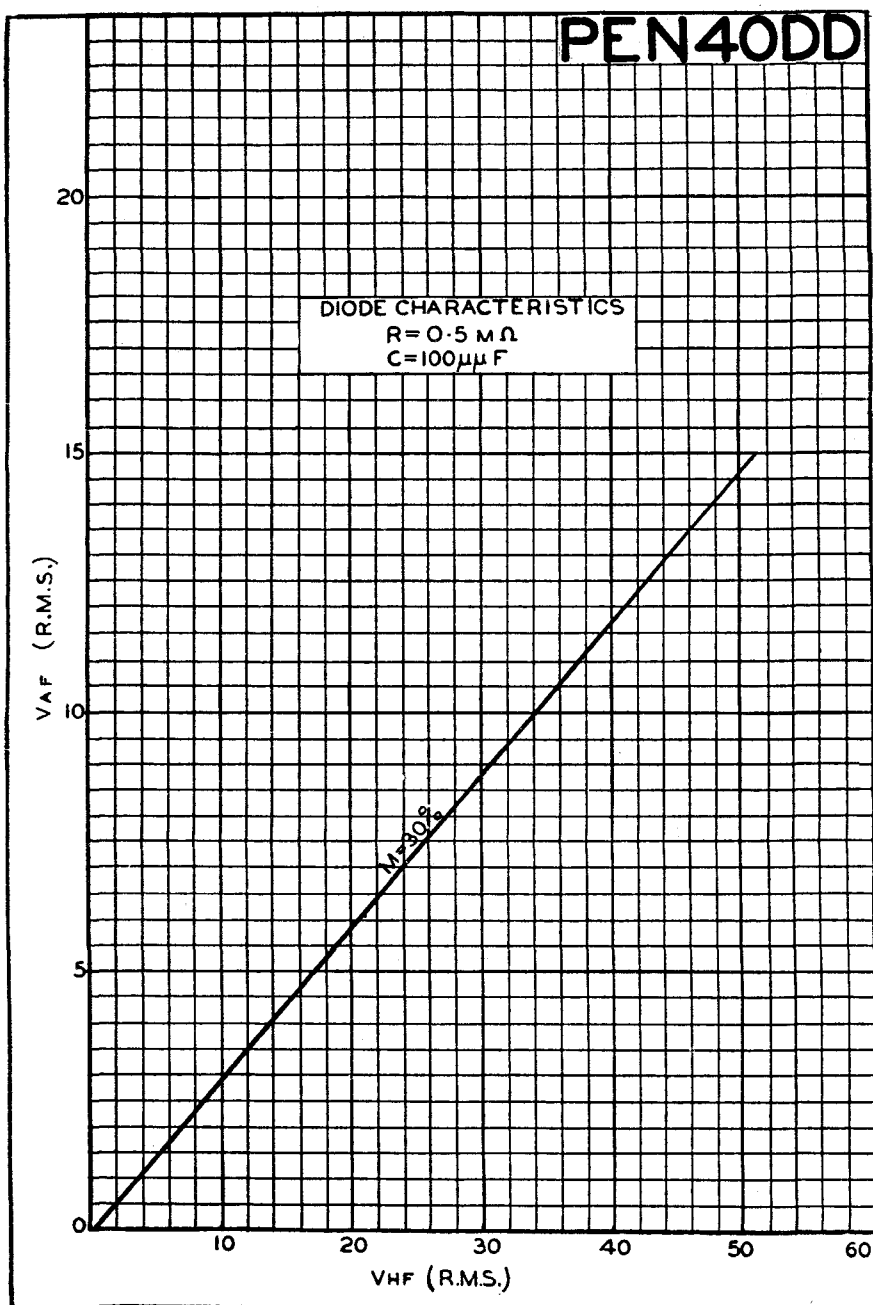
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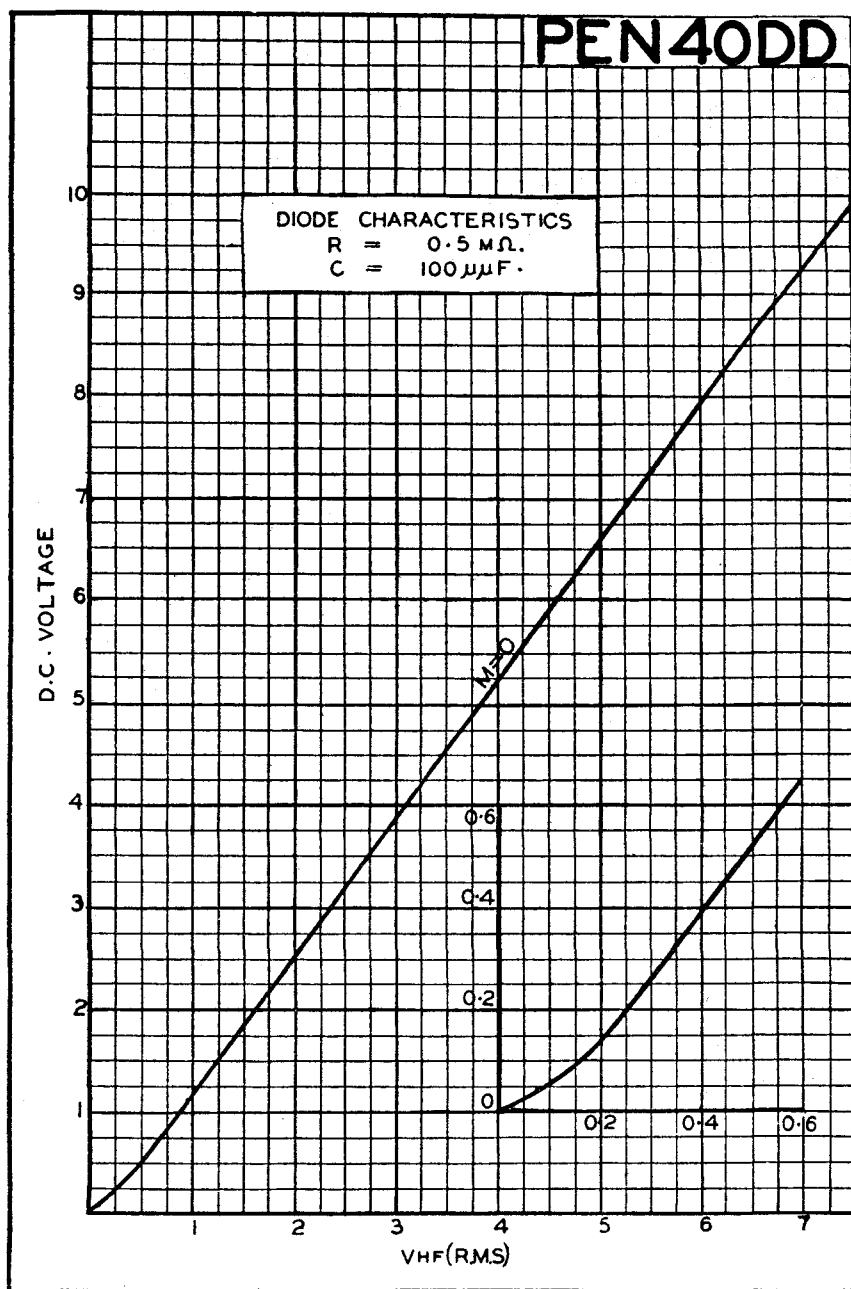
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