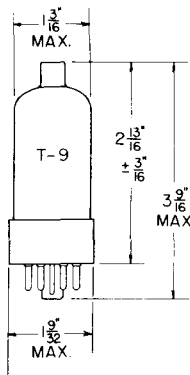


TUNG-SOL

HALF-WAVE RECTIFIER



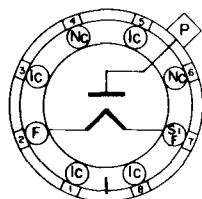
GLASS BULB

COATED FILAMENT

1.25 VOLTS 0.2 AMP.

AC
AC OR DC

ANY MOUNTING POSITION



BOTTOM VIEW

INTERMEDIATE SHELL
5 PIN OCTAL

OR

SHORT INTERMEDIATE SHELL
5 PIN OCTAL
WITH EXTERNAL BARRIERS

OR

INTERMEDIATE SHELL
6 PIN OCTAL

OR

SHORT INTERMEDIATE SHELL
6 PIN OCTAL
WITH EXTERNAL BARRIERS

OR

SHORT INTERMEDIATE SHELL
7 PIN OCTAL

OR

INTERMEDIATE SHELL
7 PIN OCTAL

THE 1G3GT IS A HALF-WAVE RECTIFIER UTILIZING A COATED FILAMENT. IT IS INTENDED FOR USE AS A RECTIFIER OF HIGH-VOLTAGE PULSES PRODUCED IN THE SCANNING SYSTEMS OF MONOCHROME TELEVISION RECEIVERS AND AS A RECTIFIER IN HIGH VOLTAGE RF-OPERATED POWER SUPPLIES OF ELECTRONIC EQUIPMENT. IT IS SIMILAR TO THE 1B3GT, BUT IS CONSTRUCTED IN A SMALLER BULB FOR COMPACT EQUIPMENT DESIGN.

DIRECT INTERELECTRODE CAPACITANCES - APPROX.
WITHOUT EXTERNAL SHIELD

PLATE TO FILAMENT AND INTERNAL SHIELD

1.3 $\mu\mu\text{f}$

CONTINUED ON FOLLOWING PAGE

TUNG-SOL

CONTINUED FROM PRECEDING PAGE

RATINGS

INTERPRETED ACCORDING TO DESIGN CENTER SYSTEM

PULSE-RECTIFIER SERVICE^A

FILAMENT VOLTAGE ^B	1.25	VOLTS
MAXIMUM INVERSE PLATE VOLTAGE:		
TOTAL DC AND PEAK (ABS. MAX.) ^C	26 000 ^C	VOLTS
DC	21 000	VOLTS
MAXIMUM PLATE CURRENT:		
PEAK	50	MA.
AVERAGE	0.5	MA.

RF RECTIFIER SERVICE

FILAMENT VOLTAGE ^B	1.25	VOLTS
FILAMENT CURRENT	0.2	AMP.
MAXIMUM PEAK INVERSE PLATE VOLTAGE (ABS. MAX.)	33 000 ^C	VOLTS
MAXIMUM PLATE CURRENT:		
PEAK	30	MA.
AVERAGE	1	MA.
FREQUENCY RANGE OF SUPPLY VOLTAGE	1.5 TO 100	KC.

^A FOR OPERATION IN A 525-LINE, 30-FRAME SYSTEM AS DESCRIBED IN "STANDARDS OF GOOD ENGINEERING PRACTICE FOR TELEVISION BROADCAST STATIONS: FEDERAL COMMUNICATIONS COMMISSION", THE DUTY CYCLE OF THE VOLTAGE PULSE MUST NOT EXCEED 15% OF ONE SCANNING CYCLE.

^B FILAMENT VOLTAGE: 1.05 MIN., 1.25 AVG., 1.45 MAX. VOLTS.

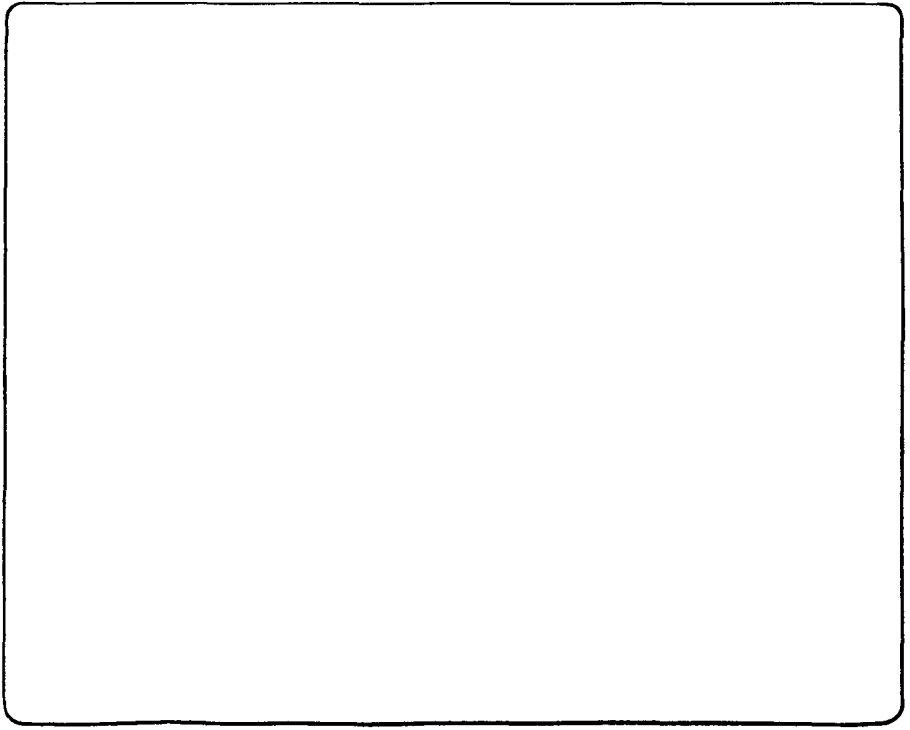
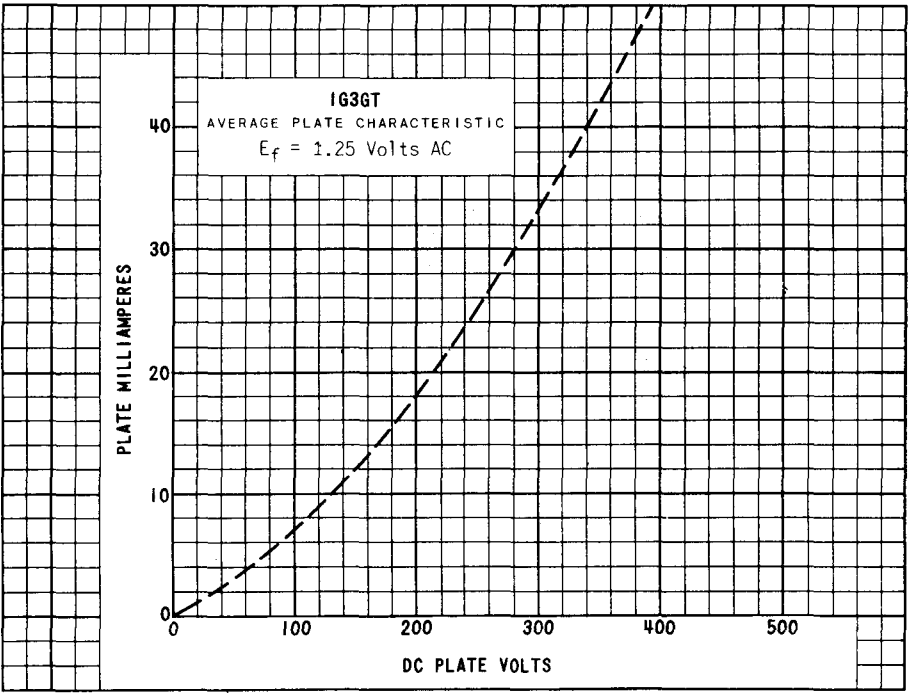
^C UNDER NO CIRCUMSTANCES SHOULD THIS ABSOLUTE VALUE BE EXCEEDED.

NOTES:

ON THE 5-PIN BASES, PIN #1 IS OMITTED.

ON THE 5-PIN BASES, THE 6-PIN BASES, AND THE 7-PIN BASE JEDEC #87-166, PIN 4 IS OMITTED.

ON THE 5-PIN BASES, THE 6-PIN BASES, AND THE 7-PIN BASE JEDEC #87-47, PIN 6 IS OMITTED.



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